1. (Amended) A system comprising:

a memory including a plurality of memory banks;

a first agent having a clock signal adapted to access a first memory portion including a first number of said plurality of memory banks; and

a second agent having a clock signal representation of said first agent's clock signal adapted to access a second memory portion including a second number of said plurality of memory banks;

said first number and said second number being variable.

7. (Twice Amended) A system comprising:

a plurality of agents;

a shared memory block accessible by each of said plurality of agents, said shared memory block including a plurality of memory banks;

a register adapted to partition said shared memory block into a plurality of partitions each of said plurality of partitions being accessible by a unique group of said plurality of agents; and

said plurality of partitions each comprise a number of said plurality of memory banks;

wherein said plurality of agents have clock signal representations of a base clock signal.